

RTL8111E/RTL8105E 48Pin Reference Schematic V103

PAGE INDEX

1. Title page
2. POWER
3. CONTROLLER
4. Optional Function
5. Change Notes

Realtek Semiconductor Corp.		
No.2, Industry E. Rd. IX, Science-Based Industrial Park, Hsinchu, Taiwan 300		
Title RTL8111E/8105E Reference circuit: Index		
Size A4	Document Number RTL8111E/8105E Reference circuit	Rev
Date:	Tuesday, November 03, 2009	Sheet 1 of 5

A

B

C

D

E

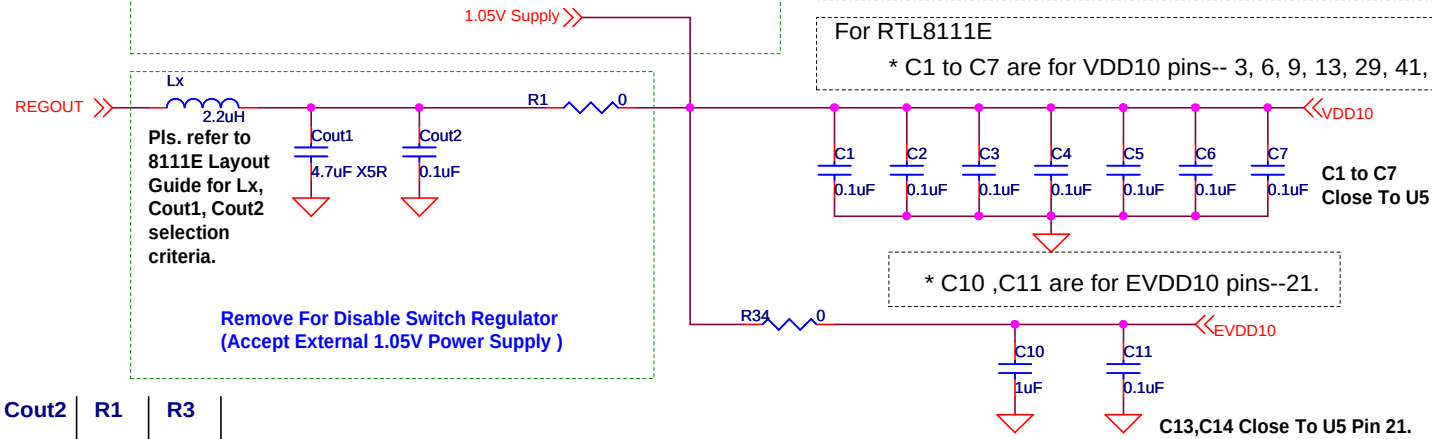
Use External 1.05V Supply When Disable Switch Regulator.
If Using External 1.2V Supply Pls. Contact With FAE.

For RTL8105E

* C1 to C4 are for VDD10 pins-- 3, 13, 29, 45.

For RTL8111E

* C1 to C7 are for VDD10 pins-- 3, 6, 9, 13, 29, 41, 45.



	Lx	Cout1	Cout2	R1	R3
Enable Switch Regulator	O	O	O	O	O
Disable Switch Regulator	X	X	X	X	X

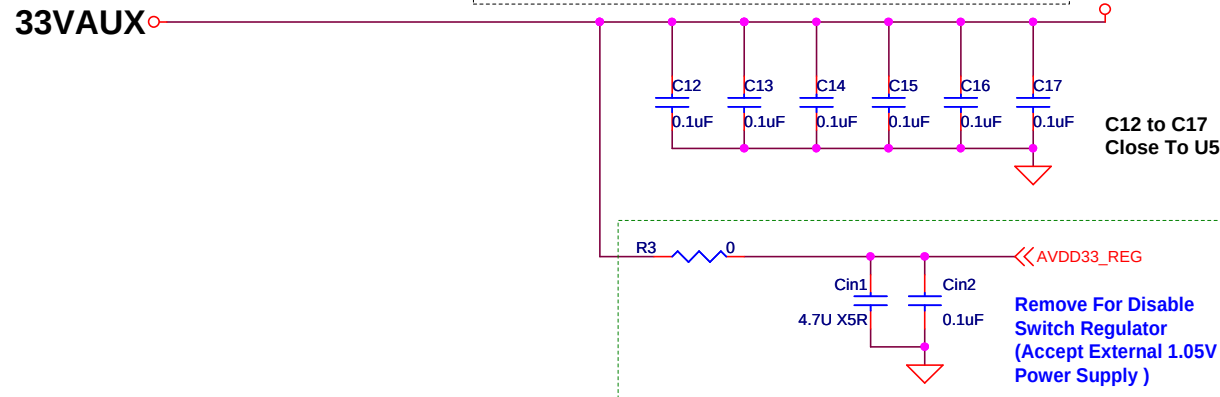
For RTL8105E

* C12 to C16 are for VDD33 pins-- 27, 39, 42, 47, 48.

For RTL8111E

* C12 to C17 are for VDD33 pins-- 12, 27, 39, 42, 47, 48.

VDD33



Realtek Semiconductor Corp.

No.2, Industry E. Rd. IX, Science-Based Industrial Park, Hsinchu, Taiwan 300

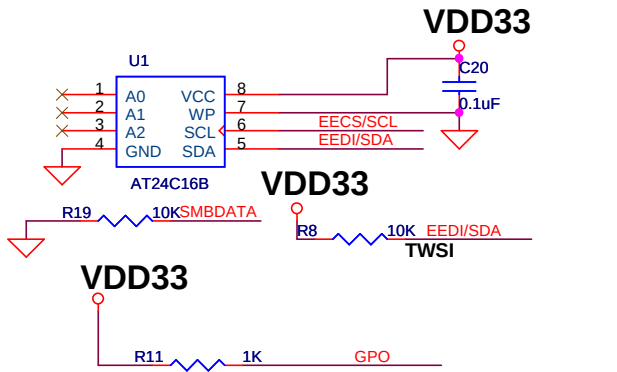
Title
RTL81111E/8105E Reference circuit: Power

Size A4 Document Number
RTL81111E/8105E Reference circuit

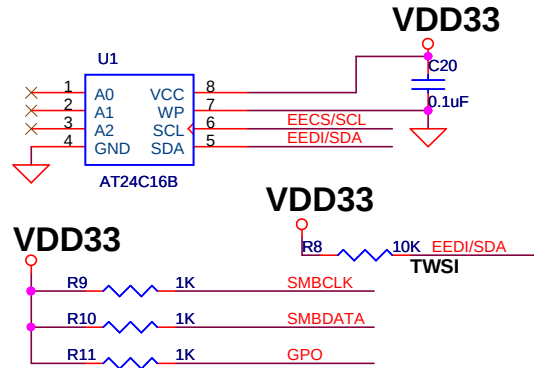
Rev

Date: Tuesday, November 03, 2009 Sheet 2 of 5

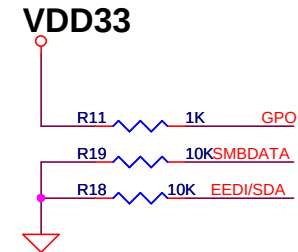
For RTL8111E TWSI EEPROM / EEPROM / ASF / Efuse Function (5 Option)



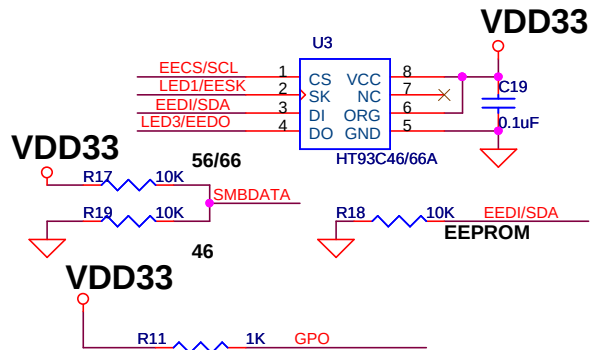
1. When using TWSI only without ASF function.



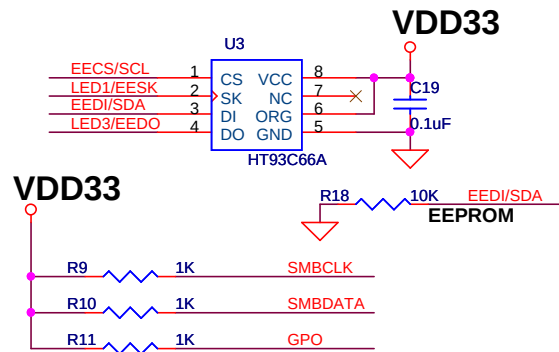
3. When Using TWSI With ASF Function



5. When using EFuse only without ASF function.

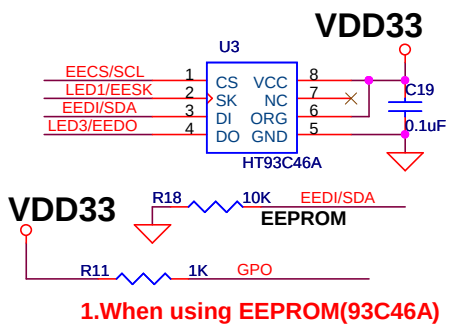


2. When using EEPROM only without ASF function.
Using R17 For 93C56/66
Using R19 For 93C46

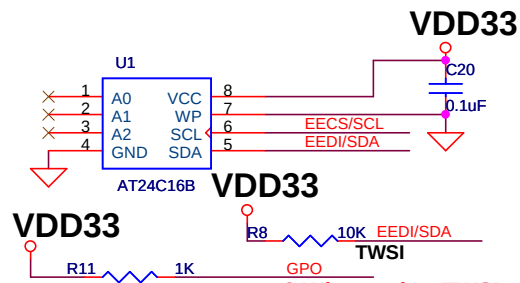


4. When Using EEPROM(Only 93C56/66) With ASF Function

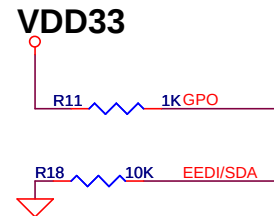
For RTL8105E TWSI EEPROM / EEPROM / Efuse Function (3 Option)



1. When using EEPROM(93C46A)



2. When using TWSI.



3. When using EFuse.

Change Notice from 1.00 to 1.01

1. C17 rename to Cin1, C18 rename to Cin2. C8 rename to Cout1, C9 rename to Cout2, L1 rename to Lx to align with SWR Layout guide (Page 2)
2. Add supported resistor values for Dual Color LED , supported combinations when using EEPROM (Page 3)
3. Add R38 for external clock circuit. (Page 3)

Change Notice from 1.01 to 1.02

1. Update Pin3 , Pin47 For RTL8105E.(Page 3.)
2. Update Bypass Capacitor Description For RTL8105E and RTL8111E. (Page 2.)
3. Modify Center tap Schematic for Power Optimization.(Page 3.)
4. Add RTL8105 Optional Function Schematic.(Page 4.)

Change Notice from 1.02 to 1.03

1. Modify Efuse Autoload Schematic. (Page 4.)

Realtek Semiconductor Corp.		
No.2, Industry E. Rd. IX, Science-Based Industrial Park, Hsinchu, Taiwan 300		
Title RTL8111E/8105E Reference circuit: Change Notes		
Size A	Document Number Change Notice	Rev
Date:	Tuesday, November 03, 2009	Sheet 5 of 5